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**SEP 29 2005**

**RESPONSE UNDER 37 C.F.R. §1.116  
EXPEDITED PROCEDURE  
EXAMINATION GROUP 2812**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s):	Meikei Ieong, et al.	Examiner:	Stanetta D. Isaac
Serial No:	10/713,971	Art Unit:	2812
Filed:	November 14, 2003	Docket:	FIS920010324US2 (15093A)
For:	METHOD AND STRUCTURE OF A DISPOSABLE REVERSED SPACER PROCESS FOR HIGH PERFORMANCE RECESSED CHANNEL CMOS		

Confirmation No.: 3181

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**AMENDMENT UNDER 37 C.F.R. §1.116**

Sir:

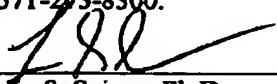
In response to the Office Action dated August 4, 2005, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

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**CERTIFICATE OF FACSIMILE TRANSMISSION**

I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office at facsimile number 571-273-8300.

Dated: September 29, 2005

  
Leslie S. Szivos, Ph.D.

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